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Lattice Extends Low Power Leadership with New Lattice Avant FPGA Platform

- Delivers industry-leading power efficiency, small form factor, and performance
- Expands product portfolio and doubles addressable market –

Dec 5th, 2022

HILLSBORO, Oregon (USA) – Dec. 5, 2022 – Lattice Semiconductor (NASDAQ: LSCC), the low power programmable leader, today unveiled Lattice Avant™, a new FPGA platform purpose-built to bring the company’s power efficient architecture, small size, and performance leadership to mid-range FPGAs. Lattice Avant offers best-in-class power efficiency, advanced connectivity, and optimized compute that enable Lattice to address an expanded set of customer applications across the Communications, Computing, Industrial, and Automotive markets.

“With Lattice Avant, we extend our low power leadership position in the FPGA industry and are poised to continue our rapid pace of innovation, while also doubling the addressable market for our product portfolio,” said Jim Anderson, President and CEO, Lattice Semiconductor. “We created Avant to address our customers’ need for compelling mid-range FPGA solutions, and we’re excited to help them accelerate their designs with new levels of power efficiency and performance.”

“Enormous amounts of data are being generated every day by billions of connected sensors, devices, and systems supported by AI algorithms, creating an accelerated need for intelligence at the Edge. This trend requires developers and OEMs to look for

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85055 Ingolstadt

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more flexible and adaptable solutions,” said Patrick Moorhead, CEO and Chief Analyst at Moor Insights & Strategy. “The introduction of Lattice Avant addresses this trend with its high-performance data processing capability where the need for innovation, efficiency, and flexibility is rapidly growing.”

Lattice Avant enables system and application developers to address important industry challenges around increases in technology connectedness and intelligence, the accelerating demand for innovation, and the growing need for efficiency and flexibility in system and application design. Combining its power optimized programmable fabric, class-leading aggregate bandwidth, adaptable hardware acceleration, and expanded application support, Lattice Avant is built from the ground up for low power FPGA leadership.

“We are proud to build on our history of power efficient innovation with the introduction of Lattice Avant by delivering breakthrough architectural advancements, features, and capabilities to address our customers’ needs across an expanded set of applications,” said Steve Douglass, Senior Vice President of R&D, Lattice Semiconductor. “Just as we’ve done with our Nexus platform, we have a strong and steady roadmap of future product introductions based on the Avant platform. To make designing with Lattice Avant FPGAs as easy as possible, Avant will be fully supported by our robust software tools and application-specific solution stacks.”

The Lattice Avant platform equips customers with:

- **Power Efficiency**
 - Up to 2.5X lower power than similar class competitive devices helping system and application engineers achieve power and thermal design efficiencies, improve operating costs, and enhance reliability
- **Performance**
 - Up to 2X faster throughput at lower power than similar class competitive devices delivers increased bandwidth and reduces links, system cost and size, while enabling datapath applications

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- **Form Factor**
 - Up to 6X smaller package size compared to similar class competitive devices enables size efficient system designs
- **Connectivity**
 - Combines configurable SERDES up to 25 Gbps, hardened support for PCIe® Gen 4, high performance I/O, and high speed memory interface support including LPDDR4 and DDR5
- **Software Support**
 - Leverages existing, familiar Lattice software solutions including easy-to-use design tools, reference designs and SDKs, a broad IP portfolio, in addition to Lattice's portfolio of application-specific solution stacks, to help customers achieve more efficient design development and faster go-to-market.

The Lattice Avant platform is architected for scalability that will enable the rapid development of multiple new device families, starting today with the launch of the Lattice Avant™-E FPGA family. Lattice Avant-E FPGAs are designed to solve key customer challenges at the Edge by combining class-leading power efficiency, size and performance with an optimized feature set tailored to the needs of Edge applications like data processing and AI.

The Lattice Avant platform and Lattice Avant-E devices were announced today at a livecast event, a replay of which will soon be available on the company's website.

For more information on the Lattice Avant, please visit:

- [Lattice Avant platform](#)
- [Lattice Avant-E FPGAs](#)

Availability and Pricing

For more information availability and pricing please visit

email: sales.europe@macnica.com.

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MACNICA ATD Europe GmbH,
82110 Munich



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Contact:

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Macnica ATD Europe GmbH

Josef Sigl

Tel. +49-89-899143-11

Email: sales.europe@macnica.com

Sales

Macnica ATD Europe GmbH

Tel. +49-84188198-102

Email: sales.europe@macnica.com

About Lattice Semiconductor

Lattice Semiconductor (NASDAQ: LSCC) is the low power programmable leader. We solve customer problems across the network, from the Edge to the Cloud, in the growing Communications, Computing, Industrial, Automotive, and Consumer markets. Our technology, long-standing relationships, and commitment to world-class support let our customers quickly and easily unleash their innovation to create a smart, secure, and connected world.

For more information about Lattice, please visit www.latticesemi.com. You can also follow us via LinkedIn, Twitter, Facebook, YouTube, WeChat, Weibo or Youku.

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About Macnica ATD Europe GmbH, (former Macnica GmbH)

Macnica's ATD Europe GmbH, (former Macnica GmbH), was originally established in the UK in 2006, and moved to Germany in July 2008, to increase efficacy of its service for European customers.

By its acquisition of the Munich based company Scantec Mikroelektronik in 2014 Macnica Europe formed a powerful semiconductor distribution with headquarter in Ingolstadt and offices in Munich, Regensburg, Milton Keynes (UK) and Warsaw offering an attractive and competitive portfolio of highly sophisticated devices.

Macnica provides end to end support from design-in to production through its global service network to its customers, regardless of the final destination of the product shipment to customers' manufacturing locations.

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About Macnica ATD Europe S.A.S.

Founded in 1990 as ATD Electronique, Macnica ATD Europe headquarter offers innovative components dedicated to imaging applications for the European market. Its product portfolio includes: image sensors (CCD, CMOS, InGaAs, Thermal etc.), optics, interface circuits, FPGA & IPs, imaging processors, cables and OLED microdisplays.

It also covers development tools and design services enabling fast and efficient realization of new high-performance camera systems for markets such as machine vision, medical, life sciences, surveillance, automotive and others. After the acquisition of the company by Macnica Inc. as of October 1, 2020 the company operates under the name Macnica ATD Europe.

About Macnica, Inc.

Macnica was established in 1972 as a semiconductor distribution company headquartered in Yokohama, Japan, and has over 85 sales offices worldwide in eastern Asia, Europe and the USA. Total number of employees is over 3,000 and its consolidated revenue for fiscal 2020 was approximately US\$ 5.5 B.

Macnica is famous for having an excellent engineering team of more than 900 application support engineers, IC designers and software developers with strong focus on providing technical support for its customers including custom design services. Macnica is continuing to extend its presence globally by having successful partners in strategic areas in the electronics market.

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